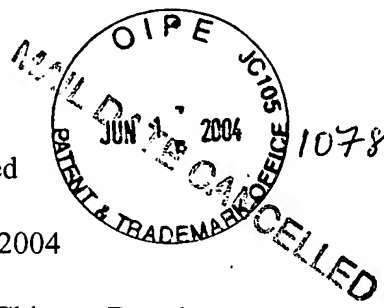


IFW

PATENT



In the Application of:

Wan et al.

Serial No. To be assigned

Filed: February 27, 2004

For: Vertical Die Chip-on-Board

10789682 Examiner: TBD

Art Unit: TBD

**TRANSMITTAL LETTER**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

In regard to the above identified application:

1. We are transmitting herewith the attached:
  - a. Information Disclosure Statement (IDS) (2 sheets);
  - b. Form PTO-1449 (1 sheets);
  - c. 1 Cited References; and
  - d. Return Receipt Postcard.
2. With respect to fees:
  - a. X No fee is required
3. Please charge any additional fees or credit overpayment to Deposit Account No. 13-2490.
4. CERTIFICATE OF MAILING UNDER 37 CFR § 1.8: The undersigned hereby certifies that this Transmittal Letter and the paper, as described in paragraph 1 hereinabove, are being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on June 24, 2004.

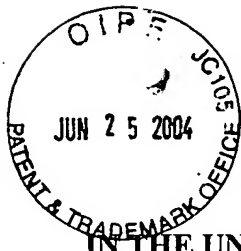
Respectfully submitted,

Date: June 24, 2004

By:

Jeffrey D. Anderson  
Reg. No. 51,403

McDonnell, Boehnen Hulbert & Berghoff LLP  
300 South Wacker Drive  
Chicago, IL 60606



**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
(Case No. 03-264-A)**

**In the Application of:**

**Wan et al.**

**Serial No. To be assigned**

**Filed: February 27, 2004**

**For: Vertical Die Chip-on-Board**

**Examiner: TBD**

**Art Unit: TBD**

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 C.F.R. Section 1.97 - 1.99, the Applicant wishes to make the following references of record in the above-identified application. This Information Disclosure Statement is in compliance with the continuing duty of candor as set forth in 37 C.F.R. Section 1.56. Copies of the references cited below are enclosed. These references are also listed on the enclosed PTO Form 1449.

In the judgment of the undersigned, portions of the listed references may be material to the Examiner's consideration of the presently pending claims. However, the references have not been reviewed in sufficient detail to make any other representation and, in particular, no representation is intended as to the relative relevance between references, whether cited in this or prior statements. This statement is not a representation that the listed references have effective dates early enough to be "prior art" within the meaning of 35 U.S.C. Section 102 or Section 103.

**Other Documents**

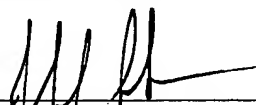
1. Van Zant, Microchip Fabrication, 4th Edition; McGraw-Hill (2000); Chapter 18, pp.557-593.

In accordance with MPEP Sections 609 and 707.05(b), it is requested the document cited (including any cited in applicant's specification which is not repeated on the attached Form PTO-1449) be given thorough consideration and that it be cited of record in the prosecution history of the present application by initialing on Form PTO-1449. Such initialing is requested even if the Examiner does not consider a cited document to be sufficiently pertinent to use in a rejection, or otherwise does not consider it to be prior art for any reason, or even if the Examiner does not believe that the guidelines for citation have been fully complied with. This is requested so that each document becomes listed on the face of the patent issuing on the present application.

Respectfully submitted,

Date: June 21, 2004

By:

  
\_\_\_\_\_  
Jeffrey D. Anderson  
Reg. No. 51,403

**McDonnell, Boehnen Hulbert & Berghoff LLP**  
300 South Wacker Drive  
Chicago, IL 60606

FORM PTO-1449  
(Rev. 2-32)

U.S. Department of Commerce  
Patent and Trademark Office

Atty. Docket No.

03-264-A

Serial No.

To be assigned

INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT  
(Use several sheets if necessary)



Applicant:

Wan et al.

Filing Date:

February 27, 2004

Group:

To be assigned

### U.S. PATENT APPLICATION DOCUMENTS

Examiner Initial		Document Number	Filing Date	Name	Class	Subclasses	Publication Date if Appropriate

### U.S. PATENT DOCUMENTS

Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate

### FOREIGN PATENT DOCUMENTS

Examiner Initial		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No

### OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc).

	1.	Van Zant, Microchip Fabrication, 4th Edition; McGraw-Hill (2000); Chapter 18, pp.557-593
EXAMINER		DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.